

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL. NUM 10020631	FILING DATE 1/07/2001	CLASS 438	SUBCLASS 1-7	CLASS 438	EXAMINER L. A. M.
**APPLICANTS: Mitros Jozef, Tord James, Pan Shanjun, Kurota Tsutomu;					
**CONTINUING DATA VERIFIED:					
**FOREIGN APPLICATIONS VERIFIED:					
E.G. PUB. DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>		ATTORNEY DOC. SET NO.	
Foreign priority claim <input type="checkbox"/>		yes <input type="checkbox"/> no <input type="checkbox"/>		71 32931	
Continued from previous application <input type="checkbox"/>		yes <input type="checkbox"/> no <input type="checkbox"/>		Verified and Acknowledged by Applicant's initials	
Title: Method for reducing low CHC degradation on MOSFET transistors					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claims for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Figs. Drawn
		Print Figs.	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
		Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)